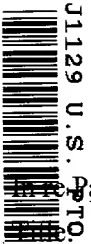


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J1129 U.S. PTO

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Patent Application of: Christophe J. Chevallier
SUPPLY VOLTAGE REDUCTION CIRCUIT FOR INTEGRATED CIRCUIT
Attorney Docket No.: 703.019US3

PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- X Return postcard.
X CONTINUATION of prior Patent Application No. 09/448,881 (under 37 CFR § 1.53(b)) comprising:
X Specification (17 pgs, including claims numbered 1 through 21 and a 1 page Abstract).
X Formal Drawing(s) (8 sheets).
X Copy of signed Combined Declaration and Power of Attorney (3 pgs) from prior application.
X Incorporation by Reference: The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
X Check in the amount of \$710.00 to pay the filing fee.
X Prior application is assigned of record to Micron Technology, Inc..
X Information Disclosure Statement (1 pgs), Form 1449 (1 pgs). References NOT enclosed, cited in prior application.
X Preliminary Amendment (2 pgs).
X Copy of Communication Re: Certificate of Merger from prior application (6 pgs.).

The filing fee has been calculated below as follows:

Table with 5 columns: Item, No. Filed, No. Extra, Rate, Fee. Rows include TOTAL CLAIMS, INDEPENDENT CLAIMS, MULTIPLE DEPENDENT CLAIMS PRESENTED, BASIC FEE, and TOTAL.

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